

Details of Compcarta EDA Tools

S. No.	Tool Name	Tool Description
1	Catalyzer	RTL to GDSII flow
2	CircuitPro	Digital schematic to Verilog, and Analog schematic to SPICE generation. Simulation framework is coming soon
3	Ignitor	Digital Design and Verification IDE
4	quickCDC	Clock domain crossing verification
5	uvmGEN	UVM-based verification environment generation
6	ATPGcore	ATPG pattern generation (Alpha version)